

first amplifier device output signal (out), triggers the signal (DataV) at the second voltage level (vddq) to change from a second back into a first state.

4. (Currently Amended) A voltage converter device ~~(101a, 101b)~~ according to Claim 3, in which the triggering flank of the first amplifier device output signal (out) is a positive flank, and the triggering flank of the second amplifier device output signal (bout) is also a positive flank.

5. (Currently Amended) A voltage converter device ~~(101a, 101b)~~ according to Claim 3, in which the triggering flank of the first amplifier device output signal (out) is a negative flank, and the triggering flank of the second amplifier device output signal (bout) is also a negative flank.

6. (Currently Amended) A voltage converter device ~~(101a, 101b)~~ according to ~~any of the above claims~~ Claim 3, in which a first transmission gate ~~(113b)~~ is controlled by the first amplifier device output signal (out), or another signal derived from it, and a second transmission gate ~~(113a)~~ is controlled by the second amplifier device output signal (bout), or another signal derived from it.

7. (Currently Amended) A voltage converter device ~~(101a, 101b)~~ according to Claim 6, in which the first amplifier device output signal (out) or a signal derived from it, is used to switch through an input of the first transmission gate ~~(113b)~~, where a relatively high voltage is present, to an output ~~(114b)~~ of the first transmission gate ~~(113b)~~.

8. (Currently Amended) A voltage converter device ~~(101a, 101b)~~ according to Claim 6 ~~or 7~~, in which the second amplifier device output signal (bout), or a signal derived from it, is used to switch through an input of the second transmission gate ~~(113b)~~, where a relatively low voltage – in particular a ground – is present, to an output ~~(114a)~~ of the second transmission gate ~~(113a)~~.

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9. (Currently Amended) A voltage converter device (~~101a, 101b~~) according to Claim 8, in which the outputs (~~114a, 114b~~) of the transmission gates (~~113a, 113b~~) are connected to each other.
10. (Currently Amended) A voltage converter device (~~101a, 101b~~) according to ~~any of the above claims~~ Claim 9, in which the first voltage level (vint) is lower than the second voltage level (vddq).
11. (Currently Amended) A voltage converter device (~~101a, 101b~~) according to Claim 10, in which the first voltage level (vint) varies from 1.2 V to 1.9 V, but more particularly from 1.4 V to 1.6 V, and the second voltage level (vddq) from 1.5 V to 2.2 V, but more particularly from 1.7 V to 1.9 V.
12. (Currently Amended) A voltage converter device (~~101a, 101b~~) according to ~~any of the above claims~~ Claim 11, in which the amplifier device (~~102~~) has several cross-connected transistors (~~104a, 104b, 106a, 106b~~).
13. (Currently Amended) A voltage converter device (~~101a, 101b~~) according to Claim 12, in which the transistors (~~104a, 104b, 106a, 106b~~) are field effect transistors.